

Facile Recrystallization Process for Efficient Triple-Cation Mixed-Halide Planar-Structure Perovskite Solar Cells

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The state-of-the-art PSCs use mixed-cation and mixed-halide perovskite compositions due to the superior qualities over the archetypal MAPbI₃¹. Adding Cs⁺ to perovskite has been shown to improve qualities including thermal stability, reproducibility, and suppression of phase segregation.²⁻⁴ In general, controlling the precise stoichiometry in a perovskite is critical for meeting its desired properties. However, it has been found purposely introduced excess PbI₂ in the perovskite causes significant influence on the intrinsic perovskite qualities. The few percent excess PbI₂ predominantly resides at grain boundaries, providing benefits of prolonged carrier lifetime, enhanced charge carrier injection, suppressed ion migrations and hysteresis, leading to improved overall device performance.⁵⁻⁸ Theories have been proposed to explain these beneficial effects, including the better energy level alignments, grain boundary passivation, and superior crystal quality and crystallinity.⁷⁻⁸ Nevertheless, there are also reports showing the residual PbI₂ can lead to degradation and fragile film structure with long-term device stability concerns.⁹⁻¹¹ Nonetheless, with most of the best performing cells still incorporating slight excess PbI₂, it is important to investigate this indefinite property function for further improving device performance and stability.

In this study, we use the Cs_{0.05}(FA_{0.85}MA_{0.15})_{0.95}Pb(Br_{0.15}I_{0.85})₃ stoichiometric perovskite structure as a platform to demonstrate a simple yet effective recrystallization method for PbI₂-rich perovskite crystals with a series of recrystallization agents. Surprisingly, for the first time, through the Grazing Incidence X-ray Diffraction (GIXRD), we found not only the excess PbI₂ in the film can be successfully removed, the perovskite had a profound recrystallization throughout the bulk, showing enhanced PL emission/lifetime, charge collection efficiency, reduced defects at grain boundaries (GBs) as well as superior device performance over 20% PCE.

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